Atty. Docket No.: NLMI.P164 PATENT

IN THE UNITED STATES PATENT OFFICE

In	Re	Pat	ent	Aı	lga	ica	tion	of:

First Named Inventor: Wright, Andrew J. Examiner: Truong, Loan

Application No.: 10/685,026 Art Unit: 2114

Filed: 2003-10-14

Title: Error Correcting Content

Addressable Memory

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Statement Under 37 CFR §3.73(b)

Sir:

NetLogic Microsystems, Inc., a Delaware corporation, hereby states that it is the assignee of the entire right, title and interest in the above-identified patent application by virtue of one or more Assignment documents

X recorded in the United States Patent and Trademark Office at Reel/Frame(s):

014612/0988 017379/0729; or

enclosed herewith.

Date June 9, 2006

The undersigned is authorized to act on behalf of the assignee at least for purposes of making the foregoing statement.

Effective immediately, please direct all further communications in the above-identified patent application to the following address:

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Respectfully submitted,

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